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PATENT AND TRADEMARK OFFICE INFORMS TION DISCLOSURE CITATION			;	114596-09-4016 09/425,401 APPLICANT				
MNFORM	BYTON DISCLO		John S. Yates, Jr., et al.					
MAY- 0 & 200	MAY 0 & 2006 WUse several sheets if necessary)				GROUP ART	GROUP ART UNIT 2194 2193		
9	\$	U.S. PATE	NT DOCUMENTS	October 22, 1777				
EXCHANGE NO	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE OF APPROPRIATE		
4	4,077,058	2/28/1978	Appell	717	162	Dec. 2, 1974		
G.	4,275,441	06/23/1981	Takeuchi	· 714	38	Mar. 27, 1979		
	4,722,050	01/26/1988	Lee	712	205	Mar. 27, 1986		
	4,831,515	05/16/1989	Kamada	712	207	Feb. 10, 1986		
	5,121,472	06/09/1992	Danish	710	67	May 31, 1989		
	5,371,894	12/06/1994	DiBrino	714	34	Dec. 23, 1993		
	5,377,309	12/27/1994	Sonobe	706	60	Nov. 27, 1991		
	5,404,473 04/04/199		Papworth	712	241	Mar. 1, 1994		
	5,675,332	10/07/1997	Limberg	341	67	Oct. 7, 1997		
	5,721,855 02/24/1998		Hinton	712	218	Jul. 12, 1996		
:	5,729,728	03/17/1998	Colwell	712	234	Sep. 6, 1996		
	5,918,251	06/29/1999	Yamada	711	207	Dec. 23, 1996		
	5,926,484	07/20/1999	Takusagawa	714	710	Oct. 28, 1996		
	6,035,120	03/07/2000	Ravichandran	717	141	May 28, 1997		
	6,065,103	05/16/2000	Tran	711	156	Dec. 16, 1997		
	6,088,793	07/11/2000	Liu	712	239	Dec. 30, 1996		
	6,128,641	10/03/2000	Fleck	718	108	Sep. 12, 1997		
	6,157,993	12/5/2000	Lewchuk	711	213	Jan. 3, 2000		
	6,199,095	3/6/2001	Robinson	709	107	Jan. 29, 1996		
	6,249,862	06/19/2001	Chinnakonda	712	218	Nov. 15, 2000		
- 14 - 12 - 1	6,256,775	7/3/2001	Flynn	717	127	Dec. 11, 1997		
	6,351,646	02/26/2002	Jellema	455	461	Apr. 20, 1998		
:	6,415,379	07/02/2002	Keppel	712	209	Oct. 13, 1999		
	6,418,460	07/09/2002	Bitar	718	108	Feb. 18, 1997		
	6,535,903	3/18/2003	Yates	718	100	Jan. 29, 1996		
	6,549,959	04/15/2003	Yates	710	22	Nov. 4, 1999		
	6,553,431	04/22/2003	Yamamoto	710	8	Jul. 21, 1999		
	6,564,339	05/13/2003	Swoboda	714	30	Jan. 14, 2000		
	6,591,340	07/08/2003	Chopra	711	118	Jun. 10, 2002		
	6,685,090	02/03/2004	Nishigaya	235	382	Jan. 26, 2001		
ļ	6,694,457	02/17/2004	McKee	714	38	Mar. 6, 2001		
(A)	6,708,173	03/16/2004	Behr	707	10	Oct. 18, 2000		
EXAMINER	Chan G	DATE CONSIDERE	ATE CONSIDERED 6-15-06					

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FORM PTO-NEW U.S. PARTMENT OF COMMERCE PARTMENT AND TRADEMARK OFFICE

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		II S DATE	ENT DOCUMENTS		12121					
EXAMINER	DOCUMENT NUMBER	DATE	NAME	FILING DATE IF APPROPRIATE						
INITIAL	6,721,941	4/13/2004	Morshed	717	127	Aug. 22, 2000				
7	6,763,452	07/13/2004	Hohensee	712	227	Jun. 24, 1999				
	6,779,107	08/17/2004	Yates	712	229	Oct. 28, 1999				
	6,789,181	09/07/2004	Yates	712	4	Nov. 3, 1999				
	6,789,263	09/07/2004	Shimada	725	119	Jun. 18, 1997				
	6,820,051	11/16/2004	Swoboda	703	28	Jan. 14, 2000				
	6,820,190	11/16/2004	Knebel	712	215	Feb. 2, 2000				
	6,826,748	11/30/2004	Hohensee	717	130	Jun. 24, 1999				
	6,934,832	08/23/2005	Van Dyke	712	244	Sep. 21, 2000				
	6,941,545	09/06/2005	Reese	717	130 130 318	May 28, 1999 Jul. 7, 1999				
	6,954,923	10/11/2005	Yates	717						
	6,978,462	12/20/2005	Adler	719		Jun. 11, 1999				
(ie)	7,013,456 03/14/2006		Van Dyke	. 7 17	130	Jun. 16, 1999				
7										
<u> </u>	OTHER DO	CUMENTS (Includin	g Author, Title, Dat	e. Pertiner	t Papers,	Etc.)				
	Dean, Profile	Me: Hardware Suppo	rt for Instruction-Le	vel Profilir	ng on Out-	of-Order Processors,"				
G	1997)	Proceedings of 30th Annual Intl. IEEE/ACM Symp. on Microarchitecture, pp. 292-302 (Dec. 1997)								
	Hollingsworth Transactions	Hollingsworth, Critical Path Profiling of Message Passing and Shared-Memory Programs, IEEE Transactions on Parallel and Distributed Systems vol. 9, no. 10, pp. 1029-1040 (October 1998)								
	Intel Corporat	Intel Corporation, Intel Architecture Software Developer's Manual, Volume 1: Basic								
	Architecture, pages 3-10 to 3-13, 7-1 to 7-3, 7-20 to 7-25; Volume 2: Instruction Set Reference pages 3-60 to 3-63, 3-240 to 3-251; Volume 3: System Programming Guide, pages 11-1 to 11-3 11-10 to 11-13, 14-1 to 14-3, 14-10 to 14-15 (1997)									
		Intel Corporation, Pentium Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to								
	Intel Corporal Programming	Intel Corporation, Pentium Processor Family Developer's Manual (1997), vol. 3: Architecture and Programming Manual, pages 3-1 to 3-3, 3-10 to 3-13, 12-1 to 12-27, 14-1 to 14-30								
	Jones, Puzzlir	Jones, Puzzling with Microcode, ACM SIGARCH Computer Architecture News, vol. 11, no. 5, pp. 8-12 (1983)								
QC		Magnusson and Werner, Efficient Memory Simulation in SimICS, Proceedings of the 28th Annual Simulation Symposium, IEEE, pp. 62-73 (1995).								
EXAMINER CLAS G. DATE CONSIDERED 6-15-06										

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTORNEY DOCKET NO. 114596-09-4016 APPLICANT		SERIAL NO. 09/425,401			
PINFORMATION DISCLO			SURE CITATION	1	John S. Yates, Jr., et al.					
MAY-0 8 2006	(Use several sheets if		necessary)	ecessary) FILING DATE			GROUP ART UNIT			
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	DOCUMENT NUMBER		DATE		COUNTRY	CLASS	SUBCLASS	YES	NO	
	<u> </u>					_				
	_									
·			CUMENTS (Including							
a l		Mueller, Rustagi and Baker: MiThOS - A Real-Time Micro-Kernel Threads Operating System, IEEE Real-Time Systems Symposium 1995: pp. 49-55 (1995)								
6		Park et al., Evaluation of Scheduling Techniques on a SPARC-Based VLIW Testbed, Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture, pp. 104-113 (1997)								
Ca		Peterson, "The Profile Naming Service," ACM Transactions on Computer Systems (TOCS), v.6 n.4, p.341-364 (Nov. 1988)								
Q.		Rizvi et al., Execution-Driven Simulation of a Superscalar Processor, IEEE Proceedings of the 27th Annual Hawaii International Conference on System Sciences, pp. 185-194 (1994)								
7							••			
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							-			
EXAMINER		Ches,	9	DA	TE CONSIDER	ED 6-1	5-06			